



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

PATENT

ATTY DOCKET NO.: 5649-1188
DATE: 03/18/2004



**UTILITY PATENT APPLICATION TRANSMITTAL LETTER
AND FEE TRANSMITTAL FORM (37 CFR § 1.53(b))**

Mail Stop PATENT APPLICATION
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Transmitted herewith for filing under 37 CFR § 1.53(b) is:

- ☒ a patent application
☐ a Continuation ☐ a Divisional ☐ a Continuation-in-Part (CIP) Application of prior application no.: _____; filed _____, the disclosure of which is incorporated herein by reference in its entirety.
☐ and claims the benefit of Provisional Application No. _____; filed _____.

Inventor(s) or Application Identifier:

Jung-hee Chung; Jae-hyoung Choi; Yun-jung Lee; Han-jin Lim

Entitled: **INTEGRATED CIRCUIT DEVICES HAVING BARRIER LAYERS BETWEEN UPPER ELECTRODES AND DIELECTRIC LAYERS AND METHODS OF FABRICATING THE SAME**

Myers Bigel Sibley & Sajovec, P.A.
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Raleigh, North Carolina 27627
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Customer No. 20792

Enclosed are:

1. ☒ Application Transmittal Letter and Fee Transmittal Form (*A duplicate is enclosed for fee processing*)
2. ☒ 11 pages of Specification (including 20 claims)
3. ☒ 5 sheets of Drawings (35 USC 113)
4. ☐ Oath or Declaration
 - a. ☐ newly executed (*original or copy*)
 - b. ☐ copy from prior application (37 CFR 1.63(d) (*for continuation/divisional*) [Note Box 6 Below]
 - c. ☐ **DELETION OF INVENTOR(S)** (*Signed statement deleting inventor(s) named in the prior application*)
5. ☐ Application Data Sheet. See 37 CFR 1.76
6. ☐ Incorporation By Reference (*useable if box 4b is checked*)

The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied under Box 4b, is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
7. ☐ Microfiche Computer Program (*Appendix*)

8. ☐ Assignment papers (*cover sheet(s) and document(s)*)
9. ☐ Applicant claims small entity status. See 37 CFR § 1.27.
10. ☒ Information Disclosure Statement, PTO-1449, and 2 references cited
11. ☐ Preliminary Amendment (*Please enter all claim amendments prior to calculating the filing fee.*)
12. ☐ English Translation Document
13. ☒ Certified Copy of Korean Priority Document Application No. 2003-17074; Filed 03/19/2003
14. ☐ Sequence Listing/ Sequence Listing Diskette
 - a. ☐ computer readable copy
 - b. ☐ paper copy
 - c. ☐ statement in support
15. ☐ An Associate Power of Attorney
16. ☒ Return Receipt Postcard (MPEP 503) (*Should be specifically itemized*)
17. ☐ Deletion of Inventor(s) (Signed statement attached deleting inventor(s) name in the prior application. See 37 CFR 1.63(d)(2) and 1.33(b).
18. ☐ Nonpublication Request under 35 U.S.C. 122(b)(2)(B)(i). Applicant must attach form.
19. ☒ Other: Application filed under 37 CFR 1.41 (c) Form

The fee has been calculated as shown below:

	Column 1 No. Filed	Column 2 No. Extra	Small Entity Rate Fee	Large Entity Rate Fee
BASIC FEE			\$385.00	\$770.00
TOTAL CLAIMS	20 - 20 =	0	x 09 =	x 18 =
INDEP CLAIMS	4 - 3 =	1	x 43 =	x 86 = \$86.00
<input type="checkbox"/> MULTIPLE Dependent Claims Presented			+ 145 =	+ 290 =
<i>If the difference in Col. 1 is less than zero, Enter "0" in Col. 2</i>			Total	Total = \$856.00

- ☒ A check in the amount of **\$856.00** to cover the filing fee is enclosed.
- ☐ A check in the amount of _____ is enclosed to cover the filing fee, PLUS the Assignment Recordation fee (\$40.00).
- ☐ Please charge my Deposit Account No. 50-0220 in the amount of _____
- ☒ The Commissioner is hereby authorized to credit overpayments or charge the following fees associated with this communication to Deposit Account No. 50-0220:
- a. ☒ Additional filing fees under 37 CFR 1.16 for presentation of extra claims.
 - b. ☒ Additional patent application processing fees under 37 CFR 1.17.

Respectfully submitted,



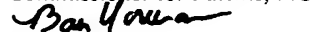
Elizabeth A. Stanek
Registration No. 48,568

CERTIFICATE OF EXPRESS MAILING

Express Mail Label No.: **EV381443384US**

Date of Deposit: **03/18/2004**

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Ban Younan

Date of Signature: **03/18/2004**

ATTORNEY DOCKET NO. 5649-1188

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Jung-hee Chung et al.

Application Serial No.: TBA

Filed: CONCURRENTLY HERewith

For: **INTEGRATED CIRCUIT DEVICES HAVING BARRIER LAYERS BETWEEN UPPER ELECTRODES AND DIELECTRIC LAYERS AND METHODS OF FABRICATING THE SAME**

Mail Stop PATENT APPLICATION

Date: March 18, 2004

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

APPLICATION FILED UNDER 37 CFR 1.41(c)

Sir:

The above-identified application is being filed on behalf of the inventors, **Jung-hee Chung**, citizen of the Republic of Korea, residing at 57-5, Cheongnyangni 2-dong, Dongdaemun-gu, Seoul, Republic of Korea; **Jae-hyoung Choi**, citizen of the Republic of Korea, residing at 108-2404, Hanjin Apt., Gwangmyeong 4-dong, Gwangmyeong-city, Kyungki-do, Republic of Korea; **Yun-jung Lee**, citizen of the Republic of Korea, residing at 1081-34, Namhyeon-dong, Gwanak-gu, Seoul, Republic of Korea; and **Han-jin Lim**, citizen of the Republic of Korea, residing at 107-405, Yeoksam Lucky Apt., Dogok 1-dong, Gangnam-gu, Seoul, Republic of Korea, under the provisions of 37 CFR 1.41(c). A Declaration and Power of Attorney from the inventors will follow, 37 CFR 1.63.

Respectfully submitted,



Elizabeth A. Stanek

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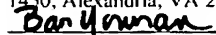
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